

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:

gate and data lines on a first substrate;

a switching device at an intersection of the gate and data lines;

a pixel electrode on the first substrate, the pixel electrode having a slit and side edge portions, the side edge portions being bent;

a common electrode on a second substrate; and

a liquid crystal layer between the first and second substrates.
2. The device of claim 1, wherein the side edge portions have convex shape.
3. The device of claim 1, wherein the slit corresponds to the data line.
4. The device of claim 1, further comprising a center electrode below the slit.
5. The device of claim 4, wherein the center electrode is formed in the same layer as the gate line.

6. The device of claim 4, wherein the center electrode is formed in the same layer as the data line.
7. The device of claim 4, wherein the center electrode is electrically connected with the common electrode.
8. The device of claim 1, further comprising a rib on the second substrate.
9. The device of claim 8, wherein the rib is located relative to the side edge portions.
10. The device of claim 1, wherein the side edge portions have concave shape.
11. The device of claim 10, further comprising a center electrode below the slit.
12. The device of claim 11, wherein the center electrode is formed in the same layer as the gate line.

13. The device of claim 11, wherein the center electrode is formed in the same layer as the data line.

14. The device of claim 10, further comprising a rib between the slit and the side edge portions.

15. The device of claim 1, wherein the liquid crystal layer has a twist angle of 10 to 80 degrees.

16. A liquid crystal display device comprising:

gate and data lines on a first substrate;

a switching device at an intersection of the gate and data lines;

a pixel electrode on the first substrate, the pixel electrode having a slit and side edge portions, the side edge portions having convex portions;

a common electrode on a second substrate;

a rib on the second substrate; and

a liquid crystal layer between the first and second substrates.

17. The device of claim 16, further comprising a center electrode below the slit.
18. The device of claim 16, wherein the rib corresponds to the side edge portions.
19. A liquid crystal display device comprising:

gate and data lines on a first substrate;

a switching device at an intersection of the gate and data lines;

a pixel electrode on the first substrate, the pixel electrode having a slit and side edge portions, the side edge portions having concave portions;

a common electrode on a second substrate;

a rib on the second substrate; and

a liquid crystal layer between the first and second substrates.
20. The device of claim 19, further comprising a center electrode below the slit.

21. The device of claim 19, wherein the rib is between slit and the side edge portions.

22. A liquid crystal display panel, comprising:

first and second substrates spaced apart from each other;

liquid crystal interposed between the first and second substrates;

a common electrode positioned on the first substrate and opposing the second substrate;

gate and data lines perpendicular to each other and positioned on the second substrate;

a thin film transistor (TFT) positioned at an intersection of the gate and data lines; and

a pixel electrode positioned on the second substrate, opposing the common electrode, and including a slit and side edge portions, the side edge portions being bent toward the first substrate.

23. The liquid crystal display panel according to claim 22, further including a center electrode, the center electrode located below the slit of the pixel and electrically connected to the common electrode.

24. The liquid crystal display panel according to claim 22, wherein the pixel electrode is located with respect to the data line such that the location of the slit corresponds with the location of the data line.

25. The liquid crystal display panel according to claim 22, wherein said pixel electrode includes a first pixel electrode portion and a second pixel electrode portion, wherein said first pixel electrode portion and said second pixel electrode portion are bordered by the slit, the liquid crystal display panel further including at least two insulated ribs located over each of the first pixel electrode portion and second pixel electrode portion, respectively.

26. The liquid crystal display panel according to claim 22, further including insulating ribs between the side edges and the slit of the pixel electrode.

27. A liquid crystal display panel, comprising:

first and second substrates spaced apart from each other;

liquid crystal interposed between the first and second substrates;

a common electrode positioned on the first substrate and opposing the second substrate;

gate and data lines perpendicular to each other and positioned on the second substrate;

a thin film transistor (TFT) positioned at an intersection of the gate and data lines; and

a pixel electrode positioned on the second substrate, opposing the common electrode, and including a slit and side edge portions, the side edge portions being bent away from the first substrate.

28. The liquid crystal display panel according to claim 27, further including a center electrode, the center electrode located below the slit of the pixel and electrically connected to the common electrode.

29. The liquid crystal display panel according to claim 27, wherein the pixel electrode is located with respect to the data line such that the location of the slit corresponds with the location of the data line.

30. The liquid crystal display panel according to claim 27, wherein said pixel electrode includes a first pixel electrode portion and a second pixel electrode portion, wherein said first pixel electrode portion and said second pixel electrode portion are bordered by the slit, the liquid crystal display panel further including at least two insulated ribs located over each of the first pixel electrode portion and second pixel electrode portion, respectively.

31. The liquid crystal display panel according to claim 27, further including insulating ribs between the side edges and the slit of the pixel electrode.

32. A liquid crystal display panel, comprising;

a first substrate having an inner surface and a second substrate having an inner surface, the inner surface of the first substrate and the inner surface of the second substrate facing one another and having a space therebetween;

liquid crystal interposed in the space;

a common electrode located on the inner surface of the first substrate;

a pixel electrode located on the inner surface of the second substrate, the pixel electrode having a slit and further having a first portion a first distance from the common electrode and a second portion a second distance from the common electrode.

33. The liquid crystal display device according to claim 32, the pixel electrode further includes a central region and side edges, wherein the first distance is defined by the distance of the central region from the common electrode and the second distance is defined by the distance of the side edges from the common electrode.

34. The liquid crystal display device according to claim 33, wherein the first distance is greater than the second distance.

35. The liquid crystal display device according to claim 33, wherein the second distance is greater than the first distance.

36. The liquid crystal display device of claim 32, further including a center electrode electrically connected to the common electrode and located between the pixel electrode and the inner surface of the second substrate in a position corresponding to the slit.

37. The liquid crystal display device of claim 32, further comprising gate and data lines located on the second substrate and perpendicular to one another, wherein the data line is located with respect to the pixel electrode such that the location of the slit corresponds to the location of the data line.

38. The liquid crystal display panel according to claim 32, wherein said pixel electrode includes first and second pixel electrode regions defined by the slit and side edges, the liquid crystal display panel further including at least two insulated ribs located over each of the first

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